

# Xilinx FPGA程序加载慢的原因和解决措施

[https://bbs.elecfans.com/jishu\\_1949548\\_1\\_1.html](https://bbs.elecfans.com/jishu_1949548_1_1.html)

如果是从spi flash启动的话，需要提高SPI的速度，具体做法就是在约束里面增加语句：

```
set_property BITSTREAM.CONFIG.SPI_BUSWIDTH 4 [current_design]
set_property CONFIG_MODE SPIx4 [current_design]
set_property BITSTREAM.CONFIG.CONFIGRATE 50 [current_design]
```