

eco commands

用于在insertion添加eco逻辑进去。

不建议使用process_top_module_connections命令，这个命令是站在顶层，然后穿层次到模块里面的信号的连接，会自动开port[]连接，不是一个好的tile flow[]

1. create_instance

2. create_module

3. create_net

4. create_port

5. create_pin

- intercept_connection

这个命令好用，用于在某个位置添加MUX或and或加buffer取反这些都很方便而且还可以抽取ICL

```
intercept_connection node -cell_function_name function_name
[-technology dft_cell_selection_name]
[-input2 input2_source] [-select select_source]
[-leaf_instance_prefix prefix_name]
[-only_when_has_functional_source]
```

6. create_connections

7. delete_connections

```
delete_connections obj_spec [-within] [-silent]
```

如果不带-within[] 是删除obj instance outside外部的连线，带-within，就是删除obj inside内部的连线。

8. move_connections

```
move_connections -from obj_spec1 -to obj_spec2 [-net_name net_name] [-silent]
```

If the object within obj_spec1 a PIN:

The command moves the net connected to this pin to the pin specified in obj_spec2 and the pin in obj_spec1 is left unconnected.

9. get_dft_cell

命令

```
get_dft_cell clock_buffer -cell_selection_name tech_name_1
```

结果

```
{my_buff}
```

命令

```
get_dft_cell and -input 4
```

结果

```
{and_4}
```

get_nets

```
get_nets [name_patterns] [-below_instances instance_objects]
[-of_pins pin_objects | -of_ports port_objects | -of_gate_pins
gate_pin_objects | -of_pseudo_ports pseudo_port_objects] [-bundle]
[-hierarchical] [-filter attribute_equation] [-regexp] [-nocase] [-silent]
```

get_nets [name_patterns] 获得net名为pattern的net

get_nets -of_pins xxxpin 获取net跟pin相对应的net net名与pin名名字一样

如果想获取pin 外面连线名,可用get_fanout -stop_on net