

1. CMOS

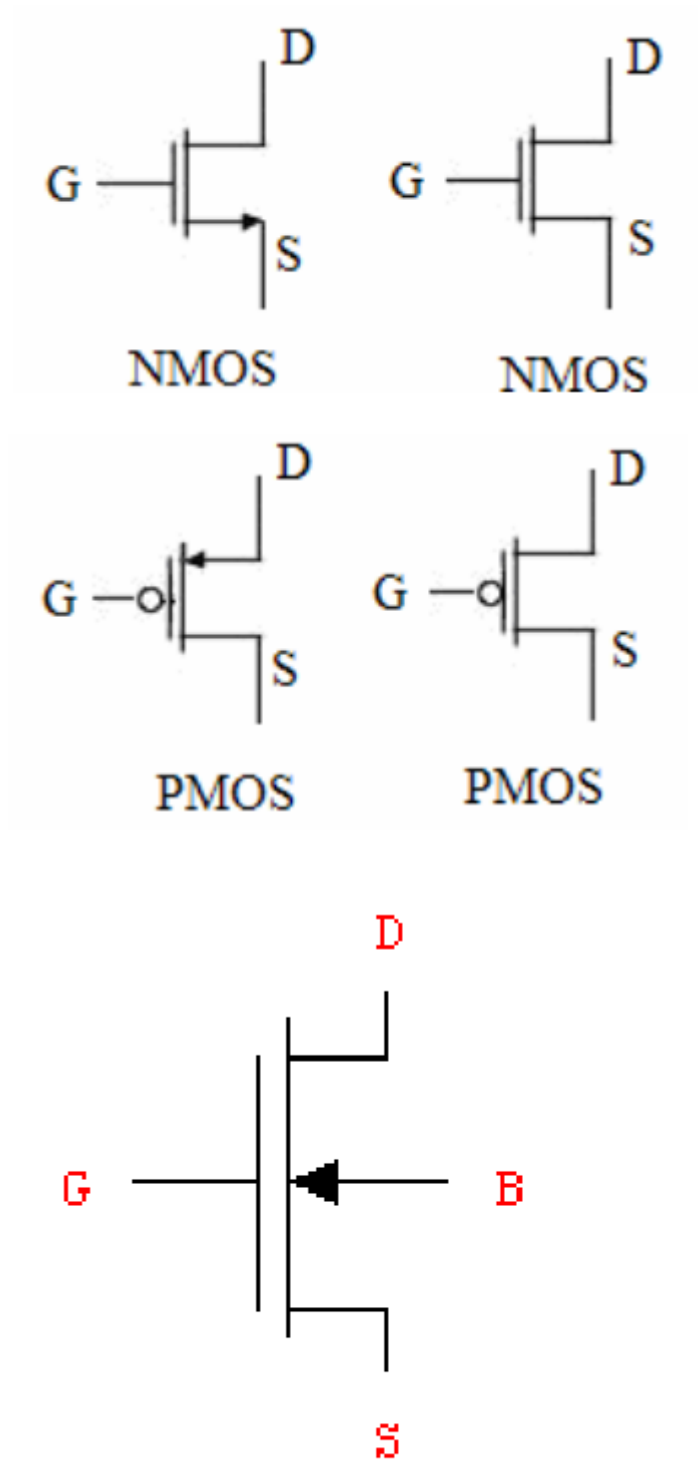
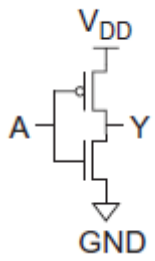
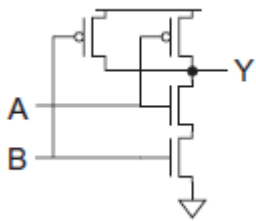


Fig 1.2.1 NMOS and PMOS (from left to right) schematic symbol

1.1 INV



1.2 NAND



1.3 NOR

